
Zynq Technical Reference

zynq ultrascale+ mpsoc software developer guide - replaced with a reference to the zynq ultrascale+ mpsoc technical reference manual (ug1085). fields and offsets table removed. replaced with a reference to the zynq ultrascale+ mpsoc technical reference manual (ug1085). added that boot access is programmable. removed several wiki sites from appendixm, additional resources and legal notices **zynq-7000 soc data sheet: overview (ds190) - xilinx** - refer to the ug585, zynq-7000 soc technical reference manual (trm) for details. 2. security is shared by the processing system and the programmable logic. 3. refer to pg054, 7 series fpgas integrated block for pci express for pci express support in specific devices. **zynq 7000 ap soc technical reference manual** - "ug585: zynq-7000 all programmable soc technical reference manual," xilinx inc., march 2013. as an example, the zynq device provides two usb 2.0 controllers, but the bora som provides (iop) interface routing" of the zynq-7000 technical reference manual. the zynq-7000 family of ap soc devices provides debug access via. **zynq-7000 soc packaging and pinout - japan.xilinx** - in the zynq-7000 soc technical reference manual (ug585) [ref 1]. table 1-5 provides definitions for all pin types. zynq-7000 socs flip-chip assembly materials are manufactured using ultra-low alpha (ula) materials defined as